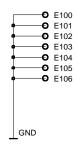
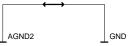


Top Sheet (2)





**DCT4 Common Baseband Schematic (Top level 2)** 

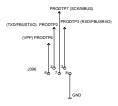






**5-pin Production Test Pattern** 

О оит





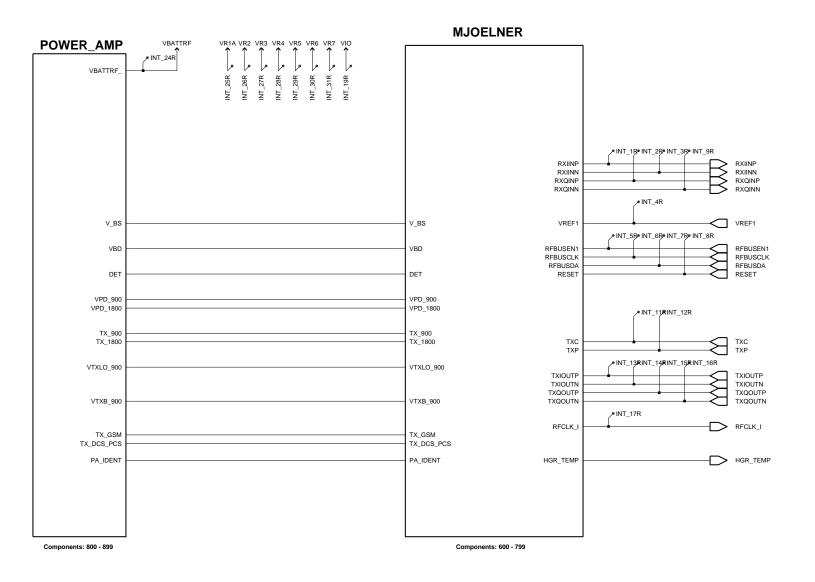
**Plug-in SIM Interface** 

RH-6

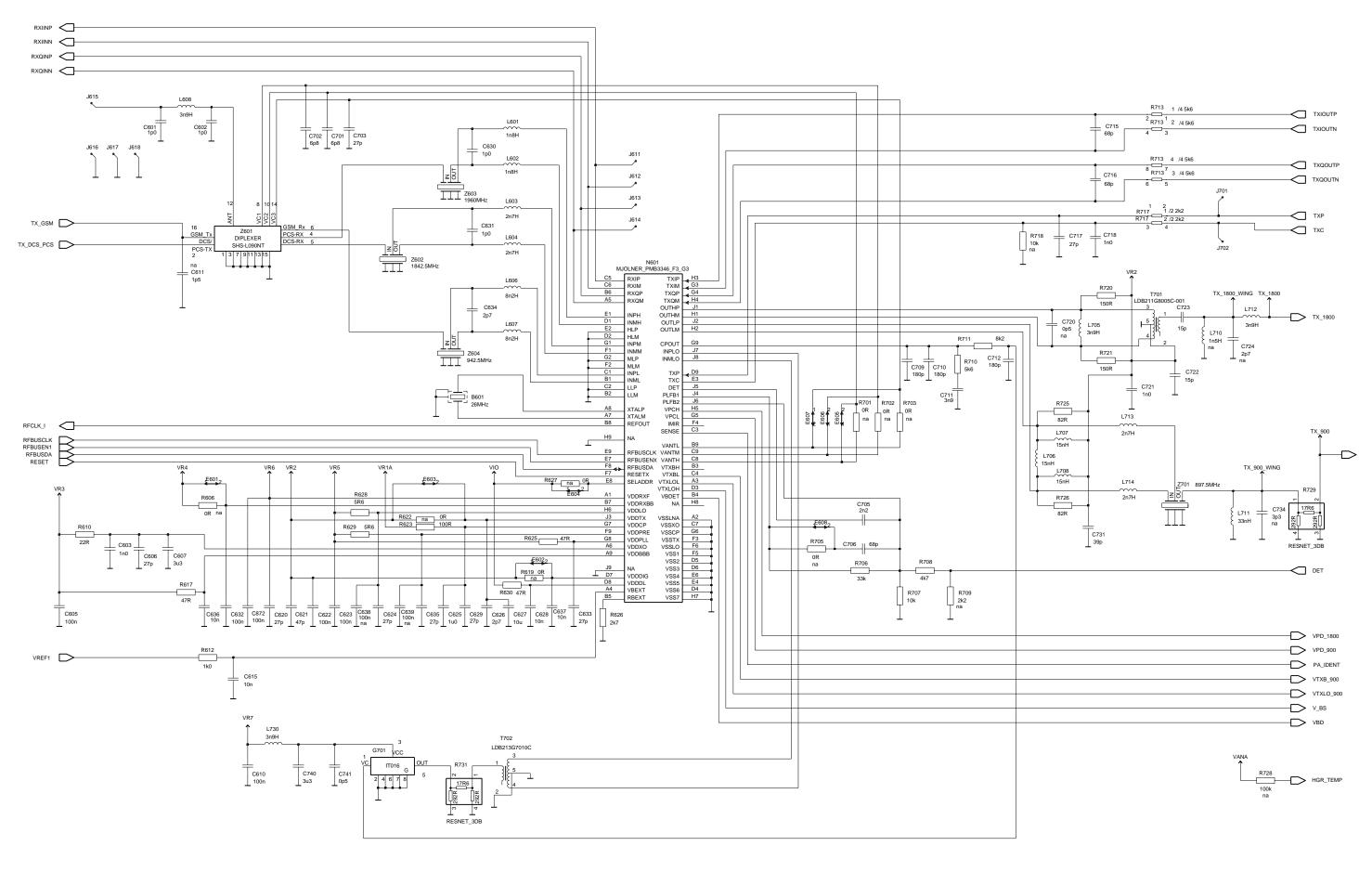
GENIO(31:0)

SIM2MMCIF(3:0)

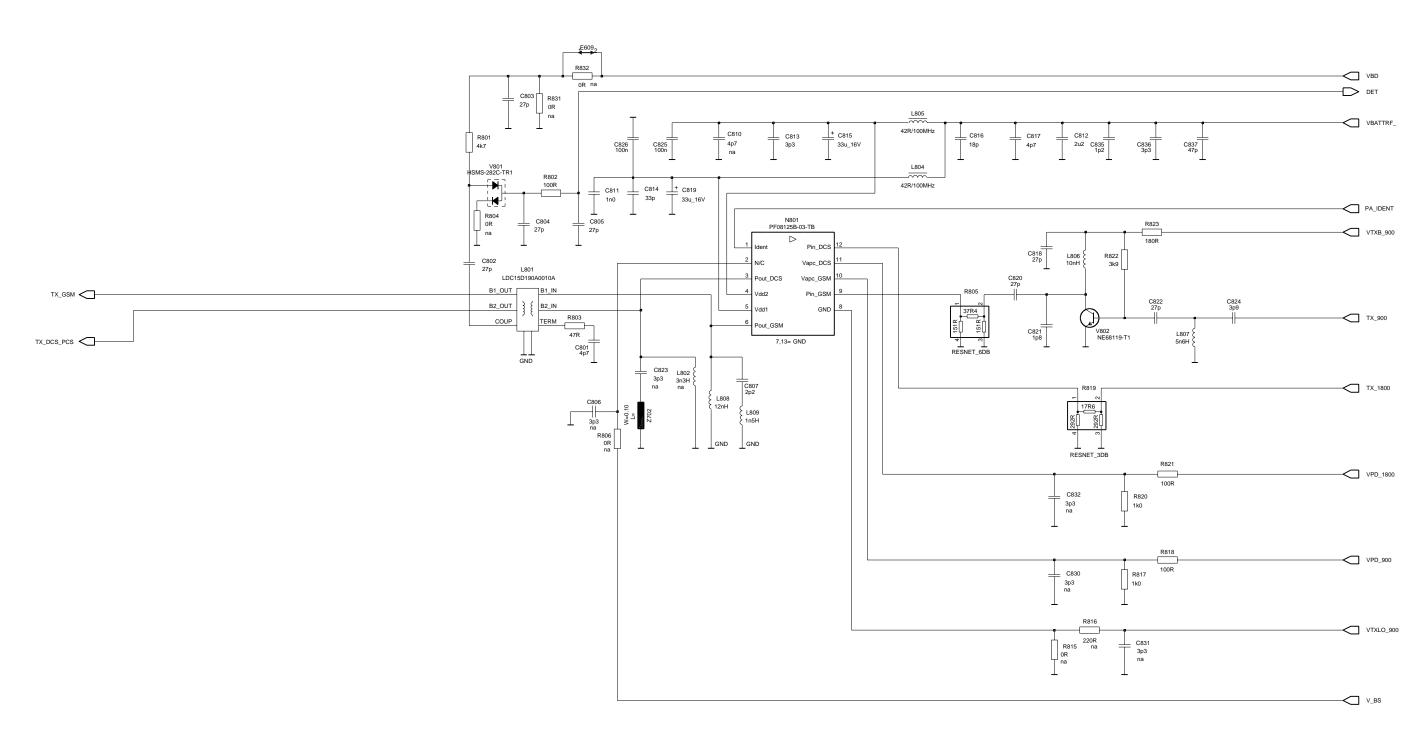
G\_SIM(7:0) 



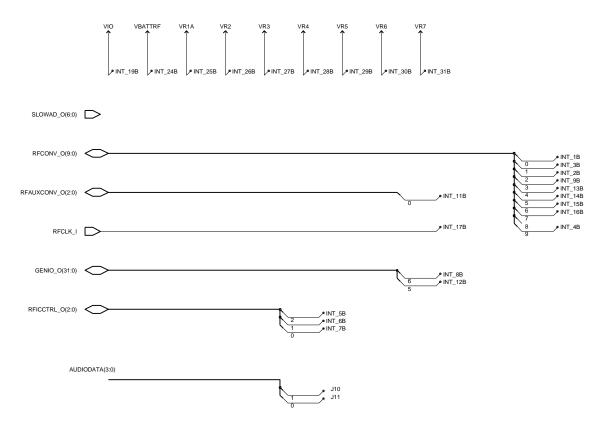
### Mjoelner



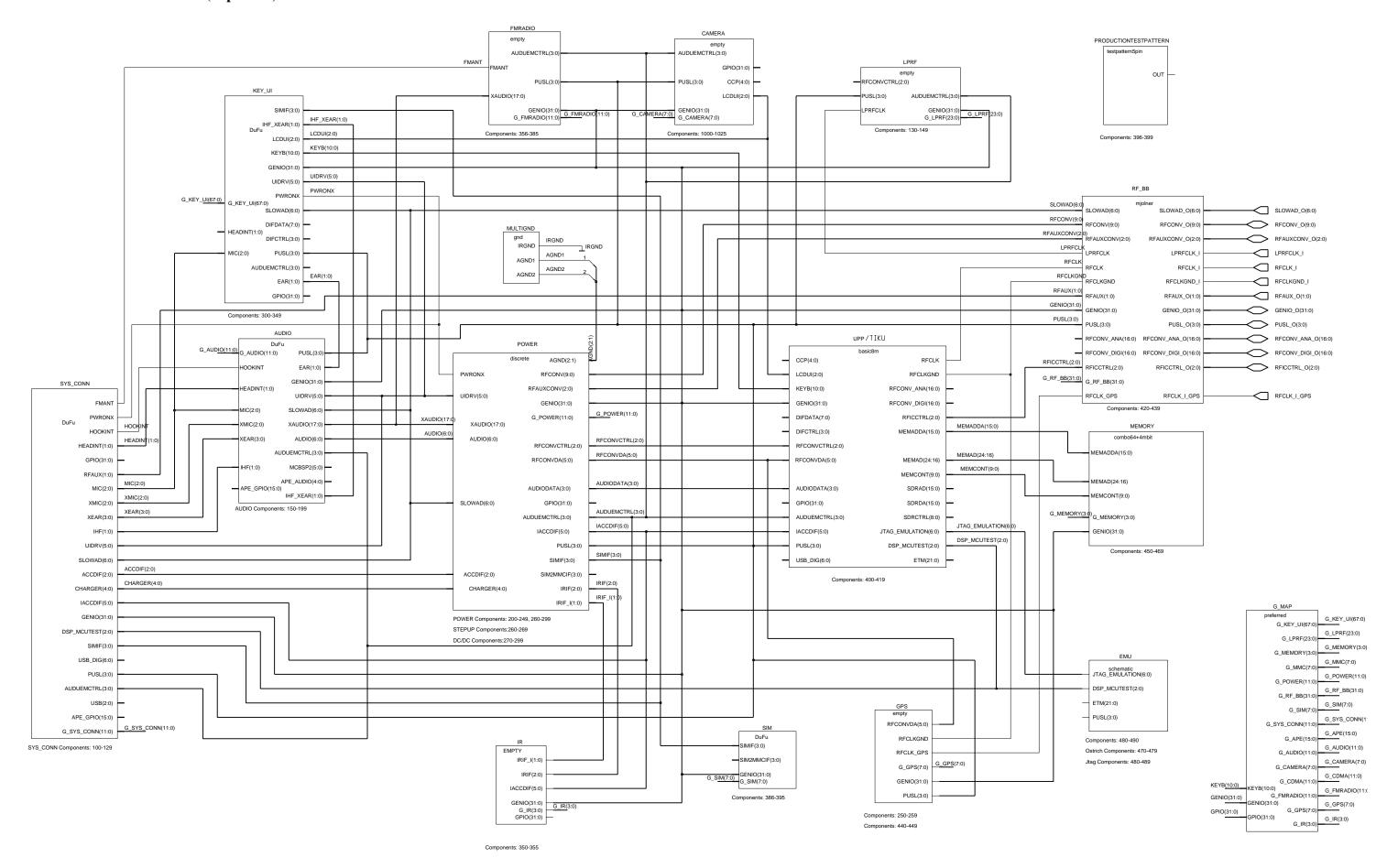
na = not assemble



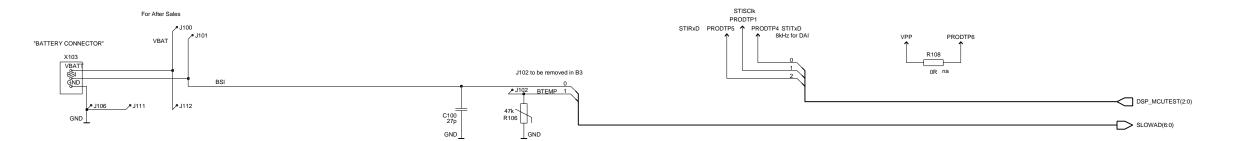
na = not assembled



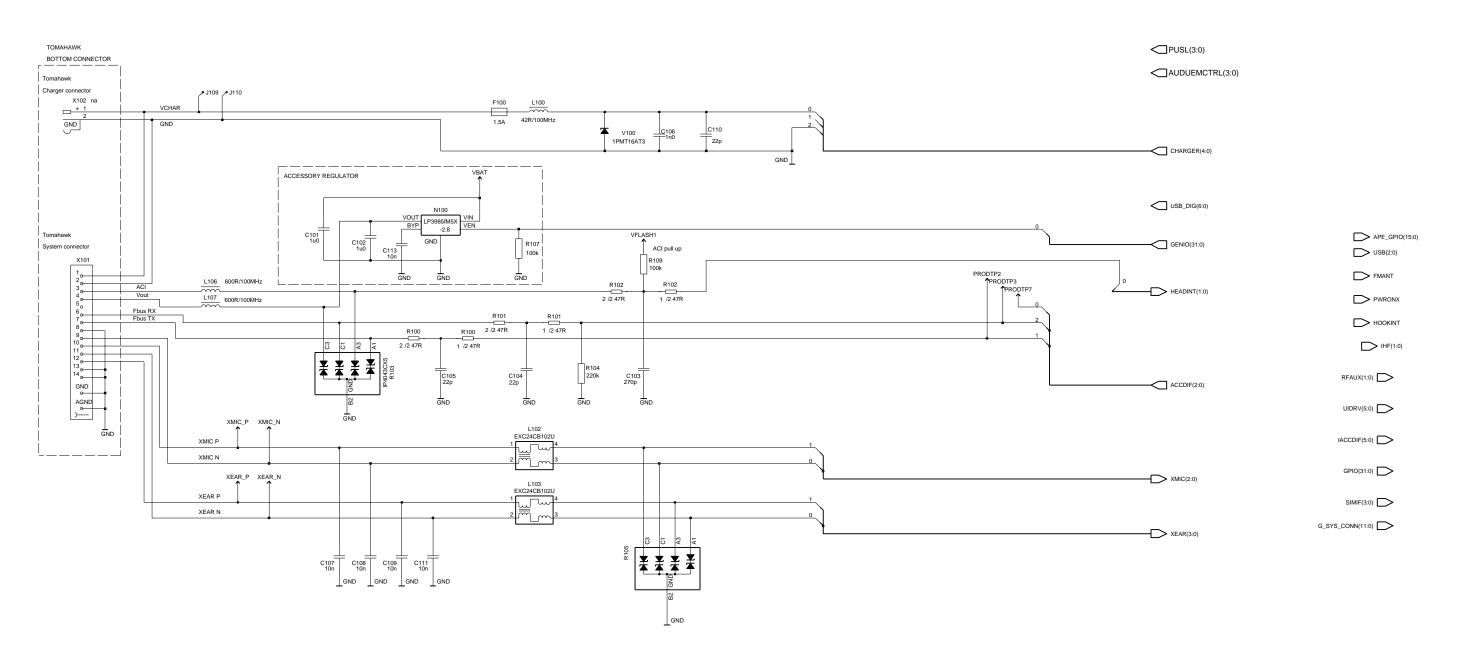
RH-6

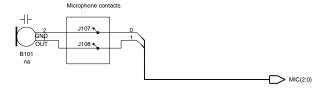


## **RH-6 System Connector**



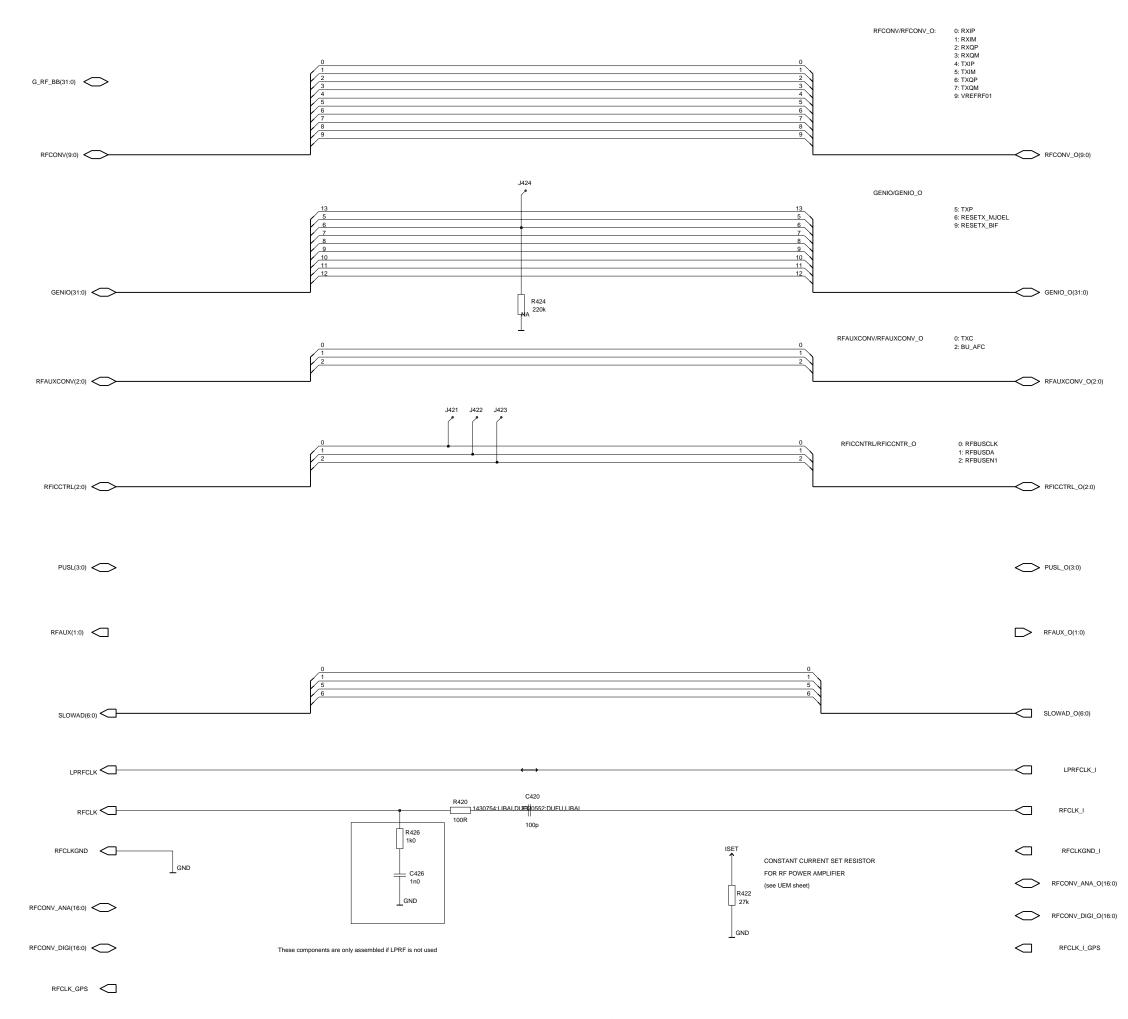
Schematics





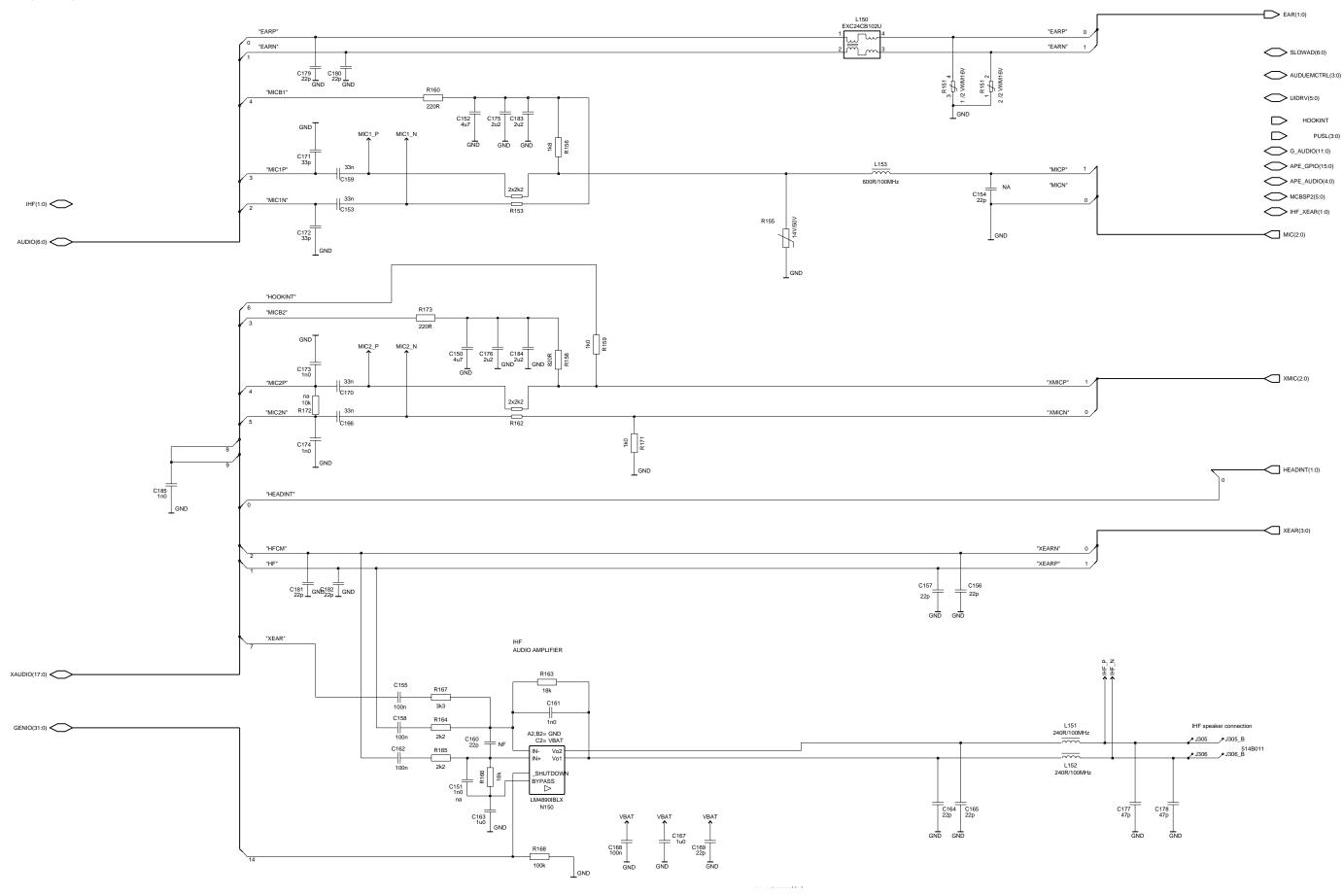
Name RH-6 System Connector Appr dc

**GSM RF-BB Interface** 



RH-6

Varistor arrays have reliability issues



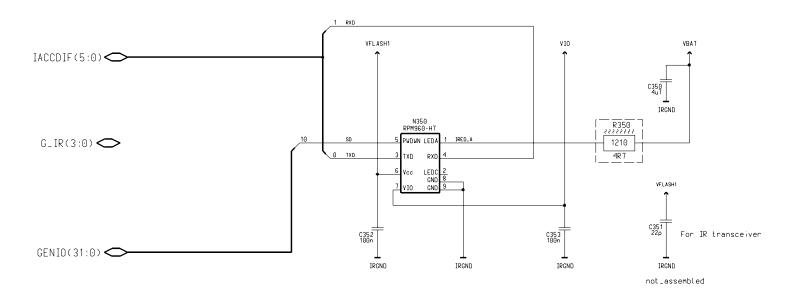
GND

GND AGND1

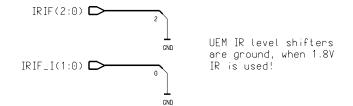
GND AGND2

RH-6

GENIO(31:0) G\_APE(15:0) GPIO(31:0) G\_AUDIO(11:0) KEYB(10:0) G\_CAMERA(7:0) G\_CDMA(11:0) G\_FMRADIO(11:0) G\_GPS(7:0) G\_IR(3:0) G\_KEY\_UI(67:0) G\_LPRF(23:0) G\_MEMORY(3:0) G\_MMC(7:0) **G\_POWER**(11:0) G\_RF\_BB(31:0) G\_SIM(7:0) G\_SYS\_CONN(11:0) NOKIA CCS Technical Documentation

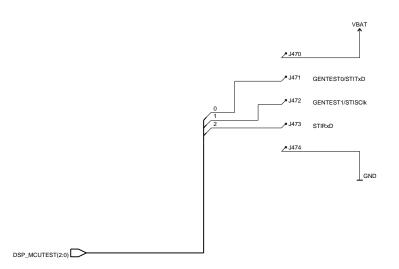


GPIO(31:0)



Schematics RH-6

**Test Points -based Ostrich Interface** 

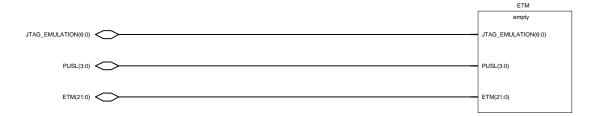


## **Test and Emulator Interface**



Schematics

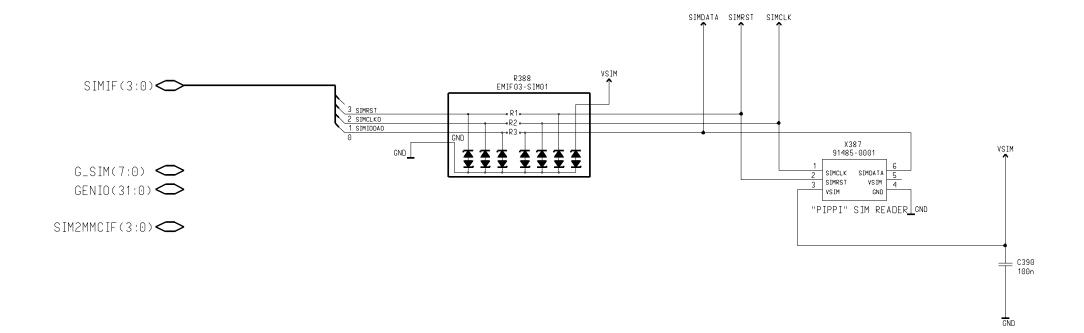




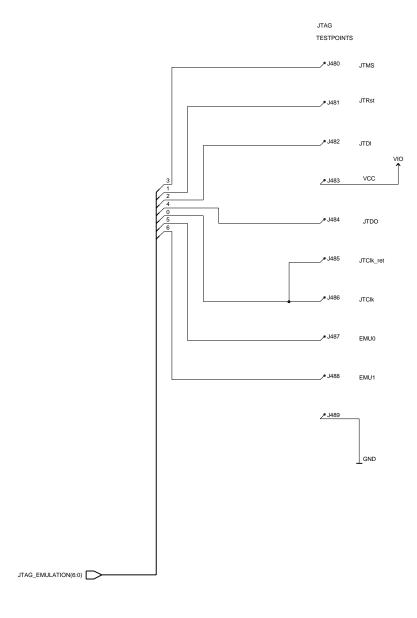
RH-6

RH-6

NOKIA CCS Technical Documentation



**Test Points for JTAG Emulator** 



VCORE\_LIN VCORE

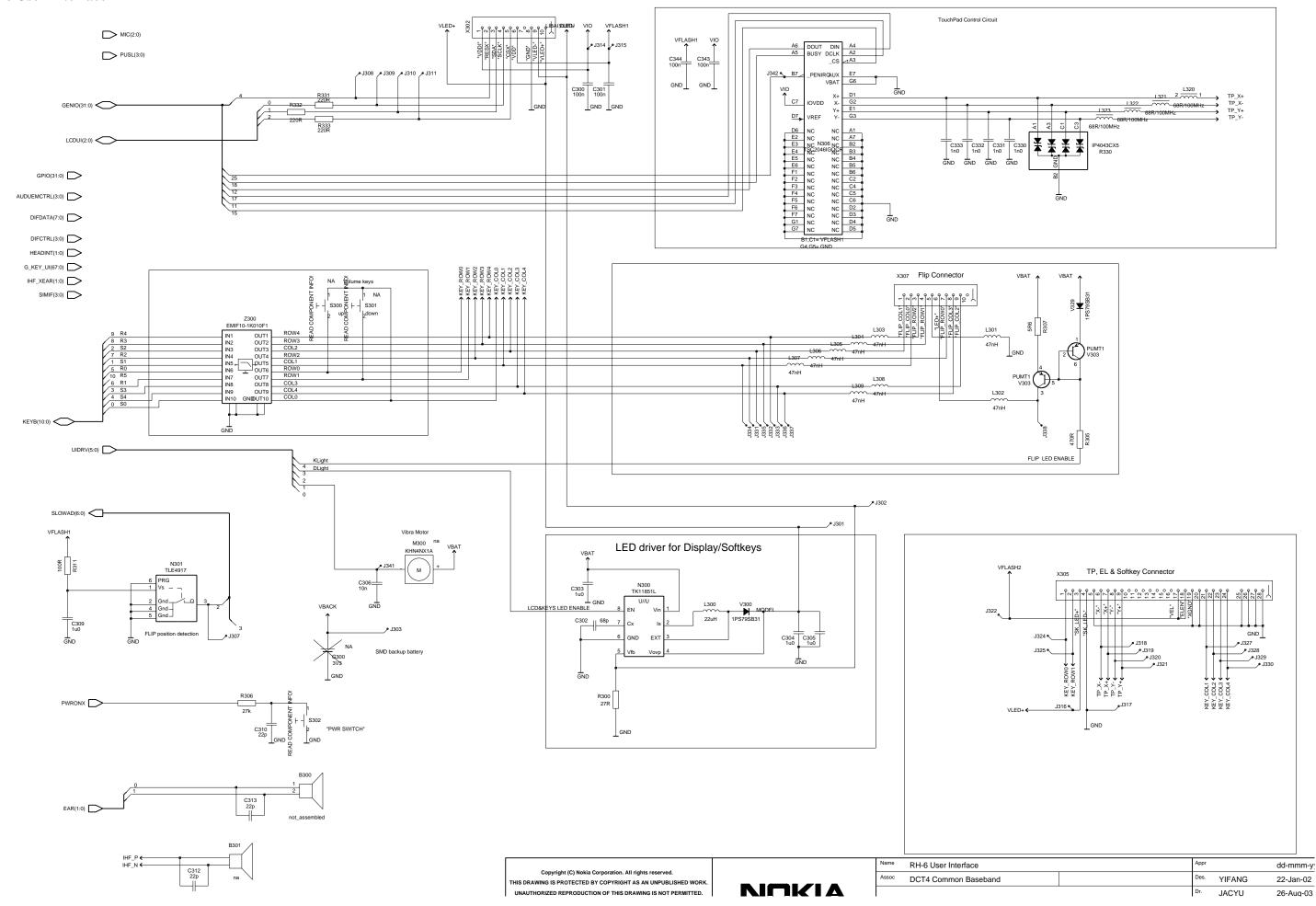
SENIO(31:0)

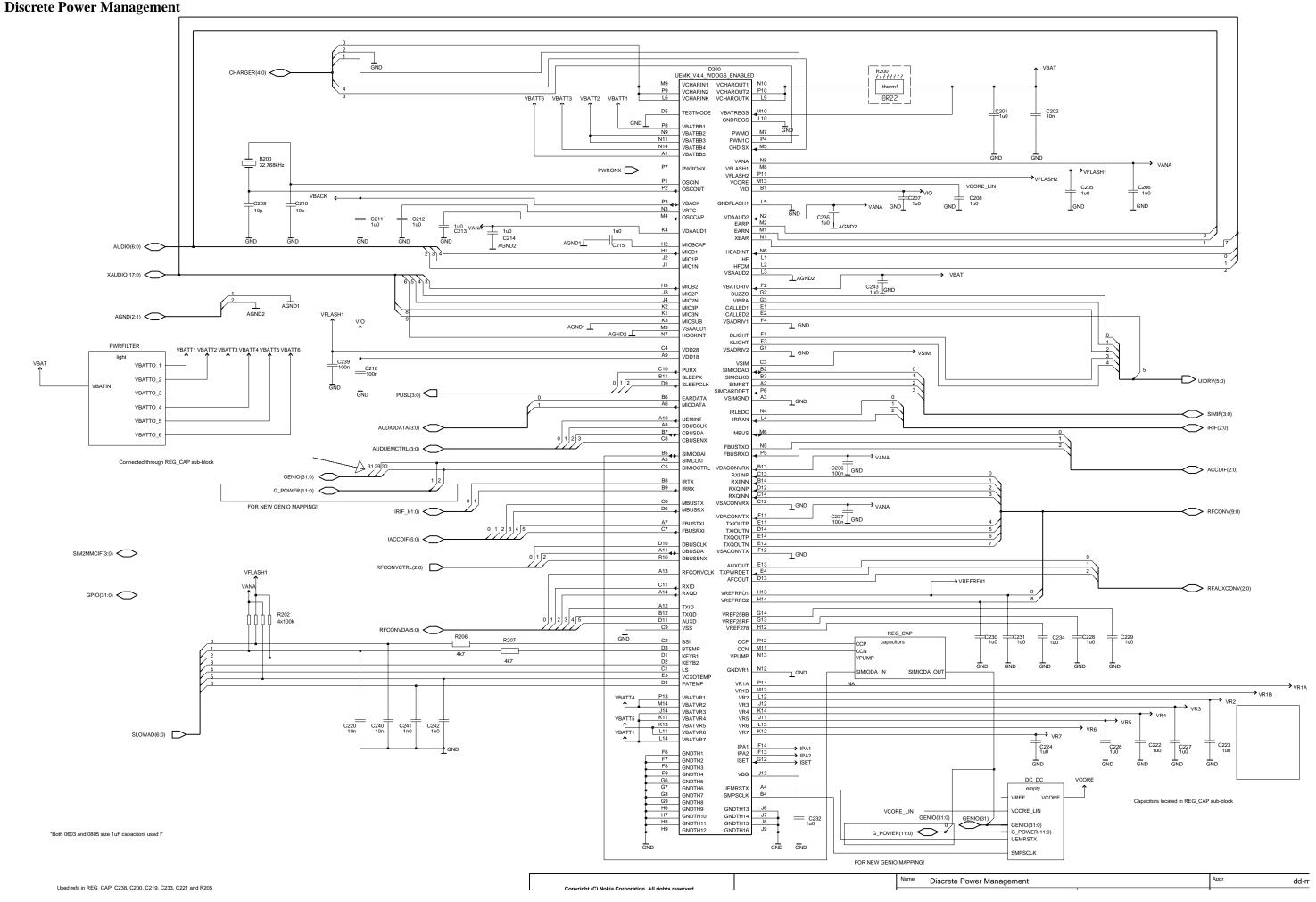
UEMRSTX

G\_POWER(11:0)

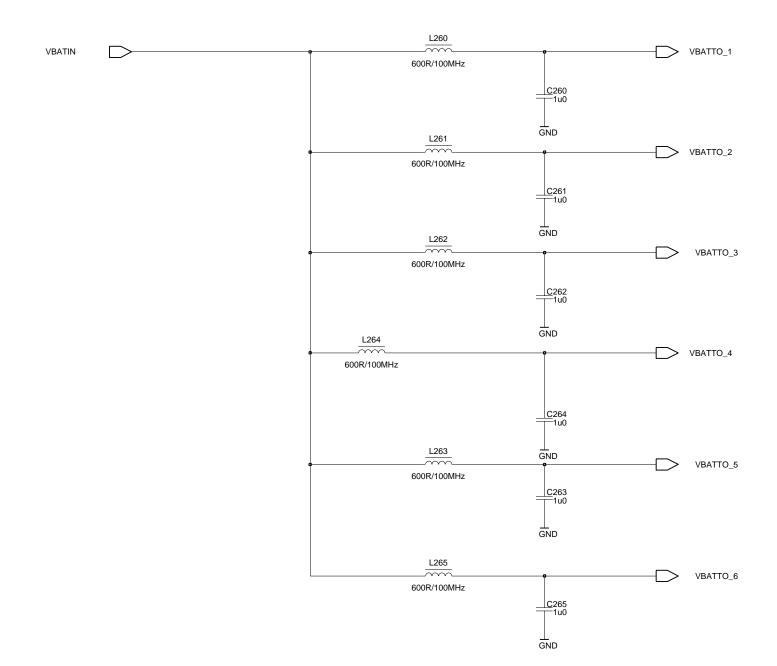
SMPSCLK

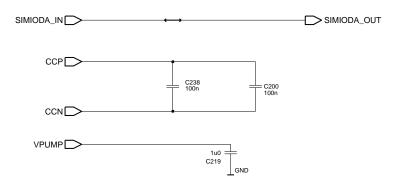
#### **RH-6 User Interface**

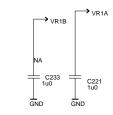


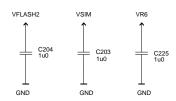


NOKIA CCS Technical Documentation

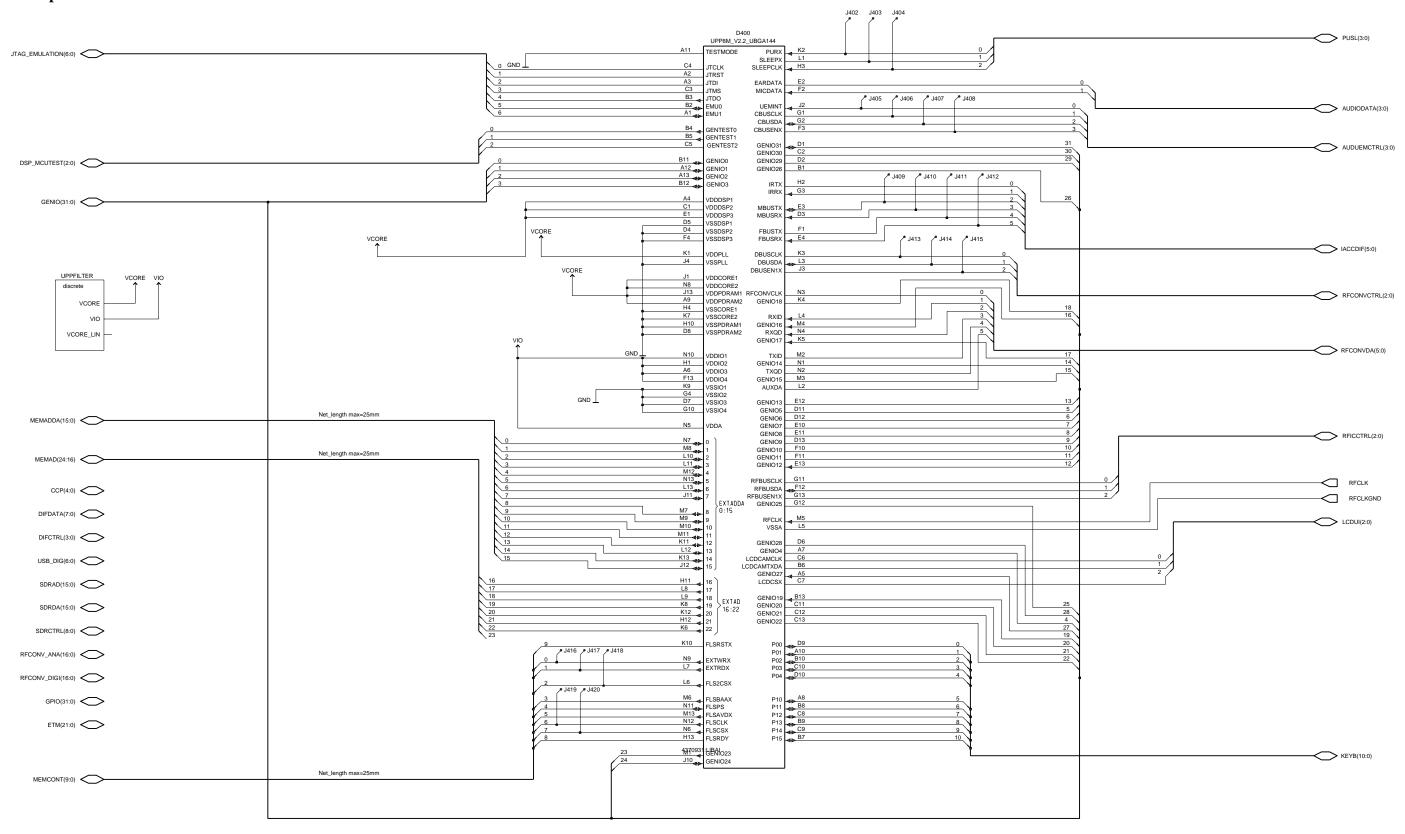


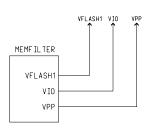


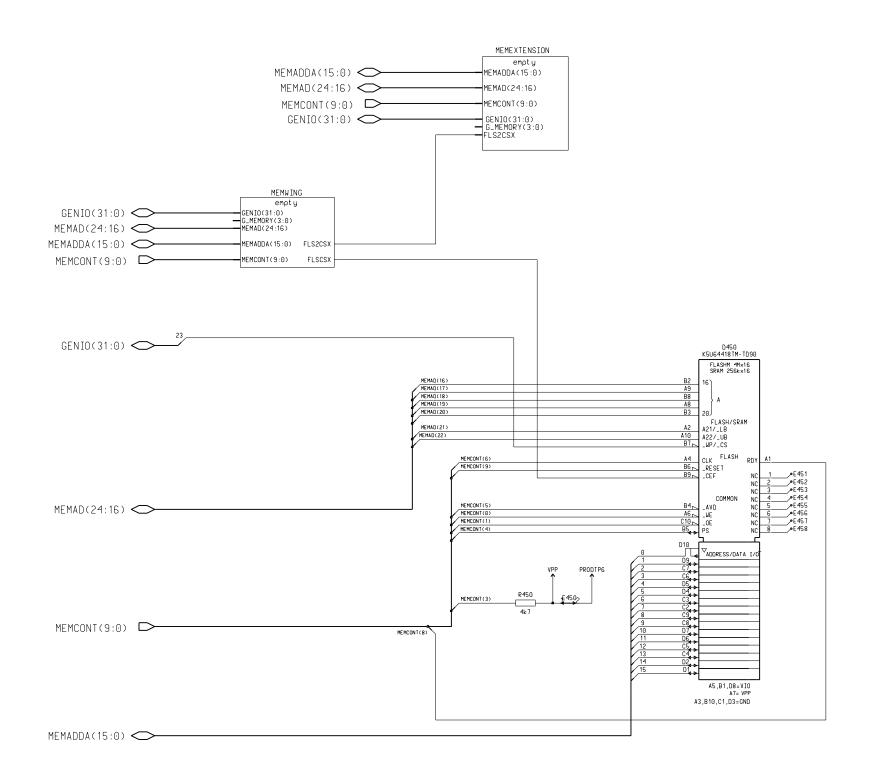








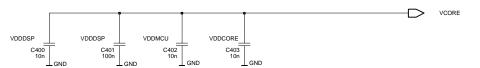




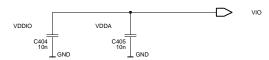
G\_MEMORY(3:0)

# RH-6

# **Discrete Decoupling Capacitors for UPP**



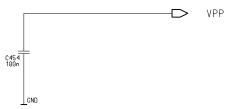
VCORE\_LIN



# Discrete Capacitors for Memory without VFlash 1



∨FLASH1



**Empty Wing Sheet** 

MEMADDA(15:0)

MEMAD(24:16)

GENIO(31:0)

G\_MEMORY(3:0) ←

FLS2CSX

MEMCONT(9:0) 

FLSCSX